

| L Number | Hits | Search Text   | DB                   | Time stamp       |
|----------|------|---|----------------------|------------------|
| 3        | 50   | pratt-thomas\$.in. mcclelland-scott\$.in.<br>stevens-craig\$.in. hopkins-kerry\$.in.<br>(118/729-731.ccls. 156/345.23.ccls.<br>156/345.31.ccls. 156/345.51.ccls.) and<br>(magnet\$3 with array) | USPAT;<br>US-PGPUB   | 2003/10/08 16:52 |
| -        | 7    | (118/729-731.ccls. 156/345.23.ccls.<br>156/345.31.ccls. 156/345.51.ccls.) and<br>(magnet\$3 with lock\$3)   | USPAT;<br>US-PGPUB   | 2003/07/09 15:11 |
| -        | 7    | (118/729-731.ccls. 156/345.23.ccls.<br>156/345.31.ccls. 156/345.51.ccls.) and<br>(magnet\$3 with lock\$3)   | USPAT;<br>US-PGPUB   | 2003/07/08 15:06 |
| -        | 7    | (118/729-731.ccls. 156/345.23.ccls.<br>156/345.31.ccls. 156/345.51.ccls.) and<br>(magnet\$3 with array)   | USPAT;<br>US-PGPUB   | 2003/07/08 15:07 |
| -        | 14   | (118/\$.ccls. 156/345.\$.ccls. 414/\$.ccls.)<br>and (magnet\$3 with rotat\$5 with lock\$3)  | USPAT;<br>US-PGPUB   | 2003/07/09 15:17 |
| -        | 0    | (c23c016\$.ipc.) and (magnet\$3 with rotat\$5<br>with lock\$3)  | EPO; JPO;<br>DERWENT | 2003/07/09 15:17 |
| -        | 8    | (c23c016\$.ipc.) and (magnet\$3 with lock\$3)   | EPO; JPO;<br>DERWENT | 2003/07/09 15:16 |
| -        | 1042 | magnet\$3 with rotat\$5 with lock\$3  | EPO; JPO;<br>DERWENT | 2003/07/09 15:18 |
| -        | 1851 | (magnet\$3 with rotat\$5 with lock\$3)  | USPAT;<br>US-PGPUB   | 2003/07/09 15:18 |
| -        | 17   | magnet\$3 with rotat\$5 with lock\$3 and<br>(wafer substrate semiconductor)   | EPO; JPO;<br>DERWENT | 2003/10/08 16:49 |
| -        | 221  | (magnet\$3 with rotat\$5 with lock\$3) and<br>(wafer substrate semiconductor)   | USPAT;<br>US-PGPUB   | 2003/07/09 15:31 |
| -        | 106  | (magnet\$3 with rotat\$5 with lock\$3) and<br>(wafer substrate semiconductor) and<br>(translat\$3 transfer\$4 transport\$3)   | USPAT;<br>US-PGPUB   | 2003/10/08 16:51 |
| -        | 24   | (magnet\$3 with rotat\$5 with lock\$3) and<br>((wafer substrate semiconductor) with<br>(translat\$3 transfer\$4 transport\$3))  | USPAT;<br>US-PGPUB   | 2003/07/09 15:32 |